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comprising at least two buses interconnected by a wireless communication network, each bus being linked to the wireless communication network by a portal, the said process being characterized in that it comprises the steps:

- of transmitting, via each portal, a synchronization signal at a predetermined instant with respect to the start of a frame and characteristic of each portal, the said frame being defined with respect to each portal's own internal clock, the said synchronization signal being achieved via the insertion of a control window;

2. Process according to Claim 1, characterized in that a control window comprises at least a part of a value of the clock of the transmitter portal of the said control window, the said value being that of the clock at the moment of transmission of the said control window, the said value transmitted being used by the receiver portal to update the value of its own clock.

4. Process according to Claims 2 or 3, characterized in that the clock value received by a portal is corrected, before the updating of the value of its own clock, so as to take account of the reception processing time of the said portal.

~~5. Process according to one of the preceding claims, characterized in that the said clock value is split up into several groups of bits transmitted over successive control windows transmitted by one and the same portal.~~

5           6. Process according to one of the preceding claims, characterized in that it furthermore comprises the step of determining, by each portal, its distance with respect to the cycle server portal, the said distance of a given portal being defined as being the minimum number of repeater portals required in order for an item originating from the cycle server portal to reach the said given portal.

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15           7. Process according to Claim 6, characterized in that the control window chosen by a given portal for synchronizing itself is the control window of a portal having the shortest distance among the control windows received by the said given portal.

20           8. Process according to one of the preceding claims, characterized in that it furthermore includes the step of locking a phase locked loop of a receiver portal to the instant of reception of the selected control window, the said phase locked loop being used to increment a register containing the said portal's own clock value.

25           9. Process according to one of the preceding claims, characterized in that it furthermore comprises the step of selecting a cycle master node of the entire communication network from among the nodes connected to the network, the cycle server portal being the portal connected to the bus to which the said cycle master node of the network is also connected, the said cycle server portal synchronizing its own clock to a clock of the said cycle master node of the network.

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35           10. Process according to Claim 9, characterized in that the communication buses being of the IEEE 1394 type, the synchronization of the cycle server portal to the cycle master node of the network is performed by way of cycle start packets transmitted by the said node, the frequency of transmission of frames over the wireless part of the network being a submultiple of the frequency of transmission of the cycle start packets.

12. Apparatus for interfacing a cable bus and a wireless communication network, characterized in that it comprises:

- means of periodic transmission to the wireless communication network of a control window serving as time reference for the other apparatuses connected to the wireless network, the generation of the said control window being bound with the state of the said counter, the said control window comprising an item relating to the state of the said counter at the moment of transmission of the said control window.

- means (40, 41, 42, 47) of clock recovery utilizing a signal transmitted over the said wireless communication network, the said recovery means comprising a phase locked loop and an own-clock register;

- means (41) for extracting a synchronization of the said control window for feeding the said phase locked loop;

- means (47) for extracting items relating to the absolute value of a reference clock of the said control window, and for updating the said own-clock register